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FORTRAN COMPILER VALIDATION SUMMARY REPORT.(U)  
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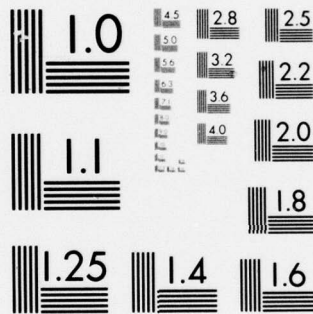
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FORTRAN COMPILER  
VALIDATION SUMMARY REPORT

VALIDATION NUMBER FCVS66-VSR200

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Prepared By:

FEDERAL COBOL COMPILER TESTING SERVICE  
DEPARTMENT OF THE NAVY  
WASHINGTON, D.C. 20376

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FORTRAN COMPILER VALIDATION

1. Validation Number	FCVS66-VSR200
2. Vendor	Sperry UNIVAC
3. Mainframe	UNIVAC 1100/43
4. Compiler Identification	UNIVAC ASCII FORTRAN Release 6R1
5. Operating System Identification	EXEC 33R1
6. Compiler Validation System Version Number	FCVS66 1.2

\*PLEASE NOTE. The Department of the Navy may make full and free public disclosure of the Validation Summary Report (VSR) in accordance with the "Freedom of Information Act" (5 U.S.C. #552). The results of this validation are only for the purpose of satisfying United States Government requirements, and apply only to the Computer System, Operating System release, and compiler version identified in the VSR. The FORTRAN Compiler Validation System is used to determine, insofar as is practical, the degree to which the subject compiler conforms to American Standard FORTRAN, X3.9-1966. Thus, the VSR is necessarily discretionary and judgmental. The United States Government does not represent or warrant that the statements, or any one of them, set forth in the VSR are accurate or complete. The VSR is not meant to be used for the purpose of publicizing the findings summarized therein.

For information concerning this compiler you can contact the vendor's designated representative named below:

Mr. H. D. Riffel  
Sperry UNIVAC  
2121 Wisconsin Avenue N.W.  
Washington, D.C. 20007



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## SECTION 1. INTRODUCTION

### 1.1 Purpose of the Validation Summary Report

The purpose of the Validation Summary Report (VSR) is to identify individual FORTRAN language elements whose implementation does not conform to the language specifications defined in American Standard FORTRAN, X3.9-1966.

### 1.2 Preparation of the VSR

The Validation Summary Report is prepared by analyzing the results of running the FORTRAN Compiler Validation System (FCVS). The FORTRAN Compiler Validation System consists of audit routines containing features of American Standard FORTRAN, their related data, and an Executive Routine which prepares the audit routines for compilation. Each audit routine is a FORTRAN program which includes many tests and supporting procedures indicating the result of the tests.

The testing of a compiler in a particular hardware/operating system environment is accomplished by compiling and executing each audit routine. The report produced by each routine tells whether the compiler passed or failed the tests in the routine. If the compiler rejects some language elements by terminating compilation, giving fatal diagnostic messages, or terminating execution abnormally, then the test containing the code the compiler was unable to process is deleted. The audit routine is compiled again and execution is repeated.

The compilation listings and the output reports of the audit routines constitute the raw data from which the members of the Federal COBOL Compiler Testing Service produce a Validation Summary Report.

### 1.3 Organization of the VSR

The Validation Summary Report is made up of several sections whose contents are described below.

a. Section 2 summarizes the results of the compilation and execution of the programs comprising the FORTRAN Compiler Validation System. Section 2 is divided into a subsection describing the syntax errors encountered while compiling the FORTRAN audit routines, and a subsection describing the semantic errors which occurred during execution of the FORTRAN audit routines.

b. Section 3 contains information which describes the software environment in which the compiler was tested. This includes the name and version of the operating system and the logical unit/physical device assignments used in the programs comprising the FCVS. The options used with the compiler are also given, and if applicable, the use of compiler optimization features is explained.

c. Appendix A is the Validation Summary Working Document, a working paper resulting from the compilation and execution of the FCVS. The VSR



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is derived from Appendix A.

#### 1.4. Use of the VSR

The Department of the Navy may make full and free public disclosure of the Validation Summary Report (VSR) in accordance with the "Freedom of Information Act" (5 U.S.C. #552). The results of the validation are only for the purposes of satisfying United States Government requirements, and apply only to the computer system, operating system release, and compiler version identified in the VSR.

The FORTRAN Compiler Validation System is used to determine, insofar as is practical, the degree to which the subject compiler conforms to the FORTRAN Standard. Thus, the VSR is necessarily discretionary and judgmental. The United States Government does not represent or warrant that the statements, or any one of them, set forth in the VSR are accurate or complete. The VSR is not meant to be used for the purpose of publicizing the findings summarized therein.

#### 1.5 Sources of Additional Information

The detailed FORTRAN language specifications are given in the publication "American Standard FORTRAN, X3.9-1966", available from the American National Standards Institute, 1430 Broadway, New York, New York 10018.

An explanation of the FORTRAN Compiler Validation System is contained in the FCVS User's Guide. This document explains how to run the compiler validation system. The User's Guide and a magnetic tape containing a copy of the FCVS programs are available from the National Technical Information Service, 5285 Port Royal Road, Springfield, Virginia, 22151. (Ordering information can be obtained from the federal COBOL Compiler Testing Service.)

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## SECTION 2. DETAILED EVALUATION OF ERRORS.

This section summarizes the results of the compilation and execution of the programs comprising the FORTRAN Compiler Validation System (FCVS). The version of the FCVS used during this validation is shown inside the front cover of the VSR.

Section 2 is made up of two subsections. The first subsection describes each syntax error encountered during compilation of the audit routines, and the second subsection describes the semantic errors encountered during execution of the audit routines.

Each error or deviation noted in this section makes reference to a program contained in Appendix A (Validation Summary Working Document). This reference provides the documented results of an occurrence of errors/deviations detected during the running of the FCVS using the compiler within the environment identified in this document. The Validation Summary Working Document is presented in sequence by program number.

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#### 2.1 Syntactical Errors

The compiler produced a fatal error message for an arithmetic assignment statement containing 57 nested parentheses. Compilation terminated upon encountering this statement. (See FMJ45.)

x3.9-1966 References: Section 6.1, Arithmetic Expressions  
Section 7.1.1.1, Arithmetic Assignment Statement

#### 2.2 Semantic Errors

No semantic errors occurred during the execution of the FCVS audit routines.

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### SECTION 3. SOFTWARE ENVIRONMENT.

The compiler referenced in this document was validated using the software environment described in this section. When using a modification of the described environment, the compiler may or may not continue to conform to the Standard. It should be noted that during the validation process, an attempt is made to validate as many different options as possible.

The use of compiler options, logical unit/physical device assignments, and any form of optimization which is not described in this report could cause the compiler to produce a program that does not perform according to the specifications of Standard FORTRAN. Only the environment described in this document has been used with this compiler to satisfy the validation requirements of the Department of the Navy.

#### 1. Options or parameters used on the processor call statement for the compiler.

Options specified:

##### a. Batch - Without Optimization.

@FTN,IS            A.PFMnnn,TEST

##### b. Batch - With Optimization.

@FTN,ISZ            A.PFMnnn,TEST

#### 2. Logical Unit/Physical Device Assignments.

Batch Compiler Runs.

Printer Destined Files:

Logical unit 06 was assigned to printer symbiont.

Tape Files:

Logical unit 07 was assigned to a tape file by using the following control card:

@ASG,T            7.,T,SCRCH

Sequential Mass-storage Files:

Logical unit 07 was assigned to a mass-storage device by using the following control card:

@ASG,T            7.,F



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Card Input Files:

Logical unit 05 was assigned to card reader symbiont.

3. Optimization. The compiler may or may not have optimization features. If there was an optimization feature available, it was used during the validation process (during a separate execution of the Compiler Validation System) to determine if its use causes the compiler to produce a program which does not give the expected results. If the optimization is invoked through the compiler call statement then it is mentioned in paragraph 1 above. If it is invoked through the introduction of a compiler directing source program statement, it is shown below. Optimization which would require modification to source program statements is not considered in this report in that it is beyond the scope of the use of Standard FORTRAN and the validation process.

The optimization feature for this compiler is invoked through the compiler call statement. See 1. above. There was no difference in the execution results when the optimization feature was invoked.

4. Compiler.

ASCII FORTRAN 6R1

5. Operating system.

EXEC 33R1



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#### APPENDIX A

#### VALIDATION SUMMARY WORKING DOCUMENT

This appendix is a working paper produced during the validation and documents the results of the compilation and execution of each of the programs comprising the FCVS. The results contained herein are based on the use of the compiler within the Validation Environment identified in this appendix. This appendix (Validation Summary Working Document) is not part of the official Validation Summary Report (VSR), and it is not intended to reflect in any way the compiler's usefulness or degree of conformance to the language specifications.

The reader of this appendix should keep in mind that the same problem area may appear in more than one program but is considered only as a single discrepancy, and the problem is reflected only once in the body of the VSR. (The VSR will in turn only reference the first occurrence of the problem in the appendix.)

This appendix is divided into four parts. The first part describes the Validation Environment. The second part lists the Monitor Input Cards used in creating a job control stream for execution in the batch mode. The third part shows the control cards required to compile and execute an individual program. The fourth part of the document is divided into two categories of information: compilation results and execution results. Information items, such as compiler warning messages, are included in the summary of compilation and execution results.

The reference document for FORTRAN is American Standard FORTRAN, X3.9-1966.

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VALIDATION ENVIRONMENT

COMPILER IDENTIFICATION:	UNIVAC ASCII FORTRAN 6R1
COMPUTER SYSTEM:	UNIVAC 1100/43
OPERATING SYSTEM:	EXEC 33R1

Each of the programs comprising the FCVS was compiled and executed twice in the batch mode. First, the programs were compiled and executed without the compiler optimization feature, and then the programs were compiled and executed with the compiler optimization feature requested. In addition, the programs which test I/O, FM100 through FM108, were run twice for both the optimized and unoptimized runs. The programs were first run with the output logical unit assigned to a tape device, and then rerun with the output logical unit assigned to a mass-storage device.

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FCVS MONITOR INPUT CARDS

The FCVS Executive Routine was used to prepare the FCVS programs for execution in the batch mode. The Monitor Control Cards used as input to the FCVS Executive are listed below:

PFM001,PFM045  
PFM050,PFM062  
PFM080,PFM083  
PFM097,PFM109

No X-cards were required since the program defaults were used for logical unit assignment.

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# CONTROL CARDS FOR RUNNING FCVS PROGRAMS

The job control stream for running the FCVS programs consisted of the following control cards:

```
@RUN      (run identification)
@DELETE,C  FCCVSSRC2.
@FREE      TPF1.
@ASG,T     TPF3.,F/1/POS/1000
@ELT,IL    MAP
IN TEST
LIB SYS$*ALTLIBS.
@ASG,CP     FCVSSRC2.,F/1/POS/5000
@FREE      FCVSSRC2.
@USE       A.,FCVSSRC2.
@FTN,ISZ    A.PFM001,TEST
```

(FORTRAN source code for program FM001)

```
@MAP,S     MAP,TEST
@XQT
```

•  
•  
•

```
@FTN,ISZ    A.PFM109,TEST
```

(FORTRAN source code for program FM109)

```
@MAP,S     MAP,TEST
@XQT
@FIN
```



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RUN SUMMARIES

FM001 through FM019

A. Compilation

No errors.

B. Execution

No errors.

FM020

A. Compilation

Compilation of the type-statement

LOGICAL LFTN01, LDTN01

and the statement function definition

LFTN01(LDTN01) = .TRUE.

produced the warning message

\*WARNING 1504 AT LINE 00035  
STMT. FUNCTION NAME 'LFTN01' IS PREVIOUSLY DEFINED.

This warning message occurred for the four logical statement functions defined in this program.

B. Execution

No errors. The statement functions flagged by the warning messages were handled correctly.

FM021 through FM044

A. Compilation

No errors.

B. Execution

No errors.

FM045



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A. Compilation

Test 759 in FM045 caused compilation to abort. The statement tests the use of 57 nested parentheses to enclose the right-hand side of an arithmetic assignment statement. The source code is:

```
IVCOMP = (((((((((((((((((((((((((((((((((((((((((((((((((((((((((((
1      IVON01 / IVON02
2      )))))))
```

The fatal message stated:

```
*ERROR 1801 AT LINE 00409
EXPRESSION IS TOO COMPLEX - SIMPLIFY
```

B. Execution

Test 759 had to be deleted. All other tests executed correctly.

FM050 through FM062

A. Compilation

No errors.

B. Execution

No errors.

FM080 through FM093

A. Compilation

No errors.

B. Execution

No errors.

FM097 through FM099

A. Compilation

No errors.

B. Execution

No errors.

FORTAN I/O Programs - FM100 through FM109

The I/O programs were executed with the output logical unit assigned

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to a tape device and rerun with the output logical unit assigned to a mass-storage device.

FM100 through FM109

A. Compilation

No errors.

B. Execution

No errors.

Optimization Runs

Each of the programs comprising the FCVS was run with the optimization feature requested for the FORTRAN processor. A warning message

\*WARNING 2502 VARIABLE 'IVCOMP' MAY BE USED BEFORE SET TO A VALUE occurred for many of the programs with different variables flagged. Some of the programs flagged several variables with the above warning message. This was the only difference in the results between the optimized and unoptimized runs.